## Notice of References Cited Application/Control No. 09/862,654 Examiner Amol V. Gole Applicant(s)/Patent Under Reexamination TOPHAM, NIGEL PETER Page 1 of 1

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,581,131 B2	06-2003	Vondran, Jr., Gary L	711/3
	В	US-6,349,372 B1	02-2002	Benveniste et al.	711/159
	С	US-6,343,354 B1	01-2002	Breternitz et al.	711/201
	D	US-5,893,143 A	04-1999	Tanaka et al.	711/120
	Е	US-5,057,837	10-1991	Colwell et al.	341/55
	F	US-5,870,576 A	02-1999	Faraboschi et al.	712/210
	G	US-6,195,107 B1	02-2001	Iverson, Timothy J.	711/203
	н	US-5,509,129 A	04-1996	Guttag et al.	712/203
	1	US-			
	J	US-			
	к	US-			
	L	US-			
	М	US-			

## FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q					
	R					
	s					
	Т					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Conte, T.M. et al., "Instruction fetch mechanisms for VLIW architectures with compressed encodings", Microarchitecture, 1996. MICRO-29. Proceedings of the 29th Annual IEEE/ACM International Symposium on, Dec. 2, 1996, pp 201-211.
	> <sup>*</sup>	Larin, S.Y.; Conte, T.M., "Compiler-driven cached code compression schemes for embedded ILP processors", Microarchitecture, 1999. MICRO-32. Proceedings. 32nd Annual International Symposium on, Nov. 16, 1999, pp 82-92.
	W -	A. Tannenbaum, "Structured Computer Organization," Prentice-Hall, 1984, pp. 10-12
	×	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.